

FIG. 1

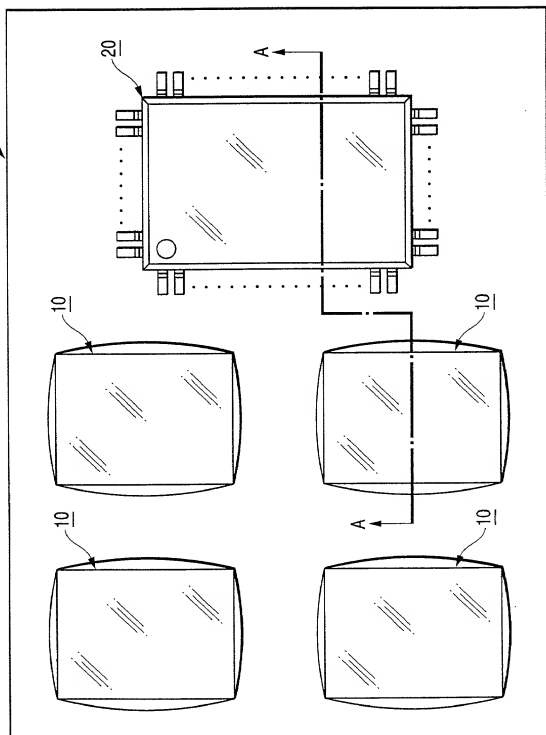


FIG. 4(A)

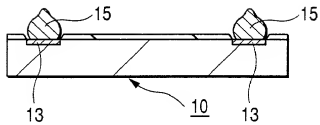


FIG. 4(B)

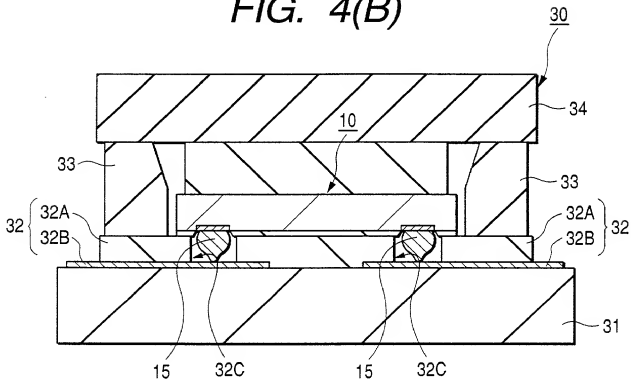


FIG. 4(C)

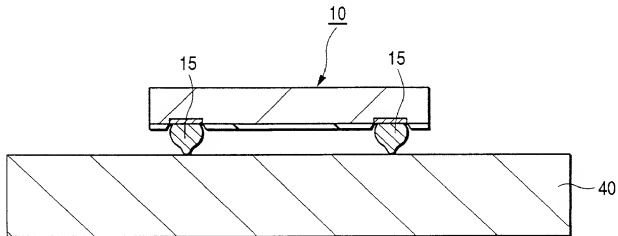


FIG. 5

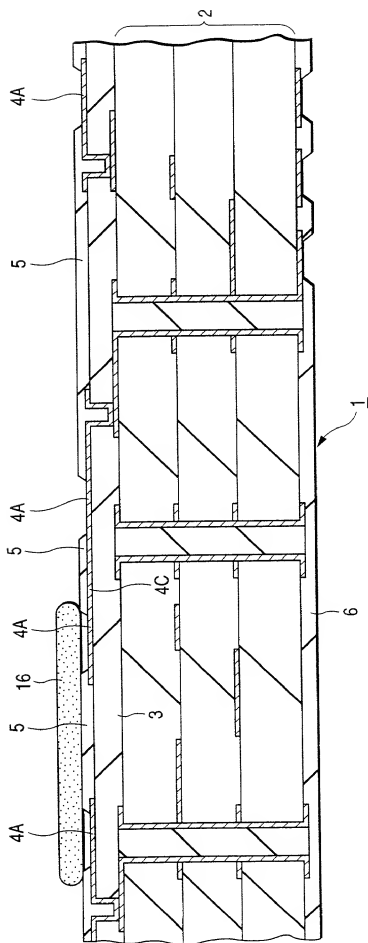
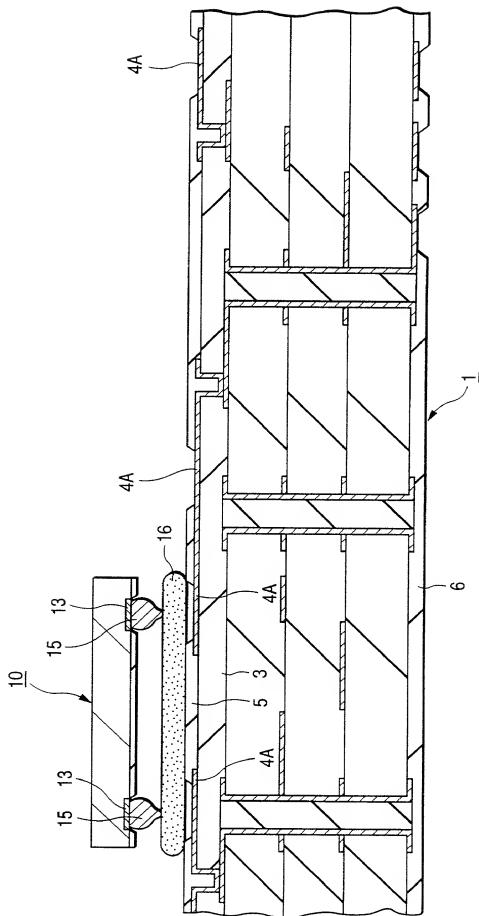
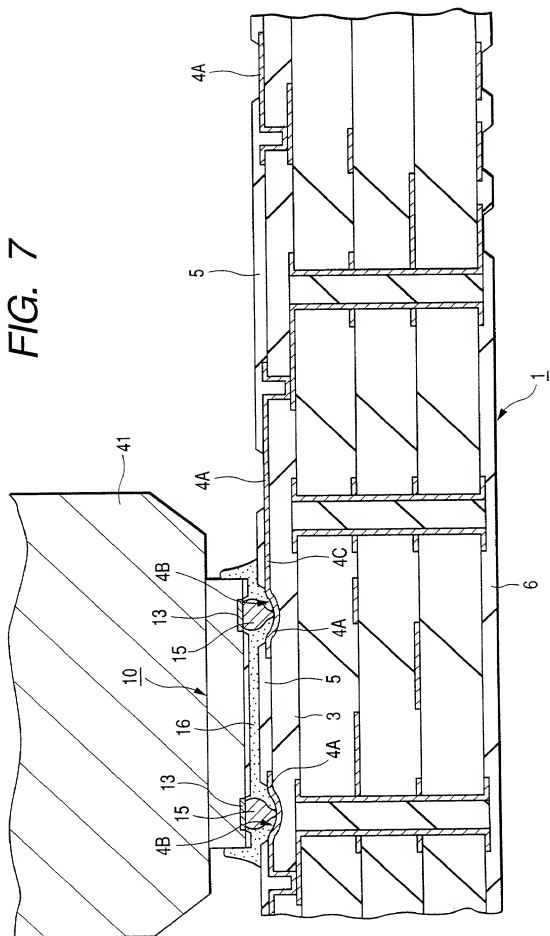


FIG. 6





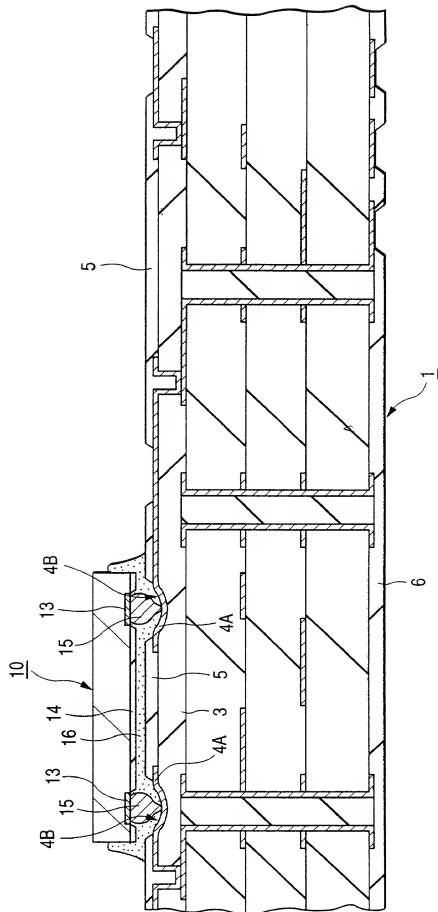


FIG. 9

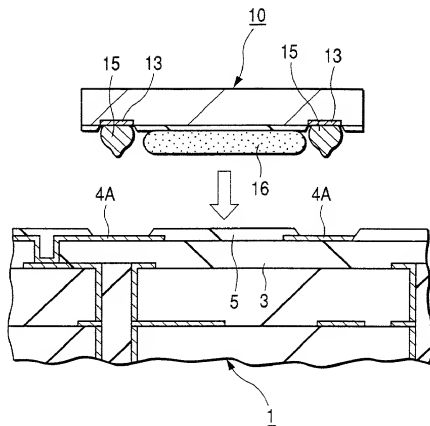


FIG. 10

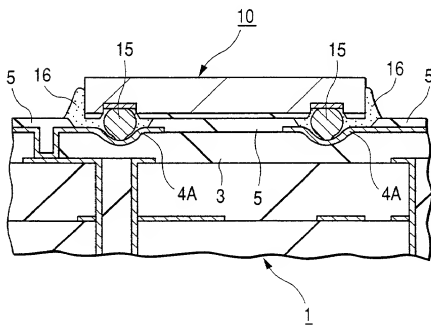


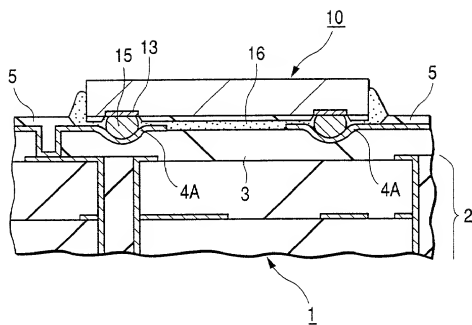
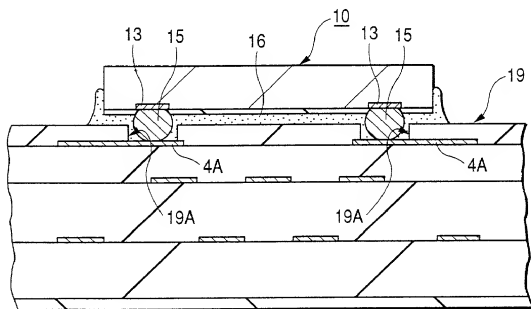
FIG. 11*FIG. 12*

FIG. 13

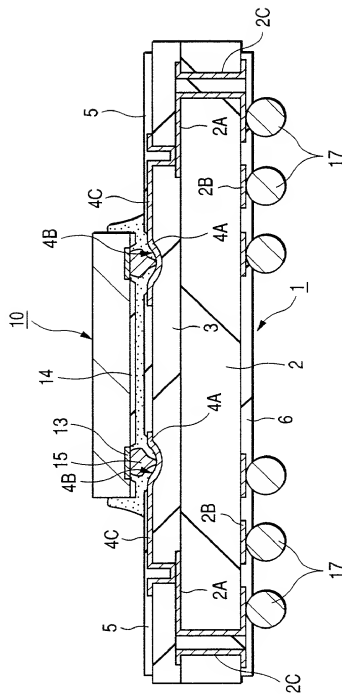


FIG. 15

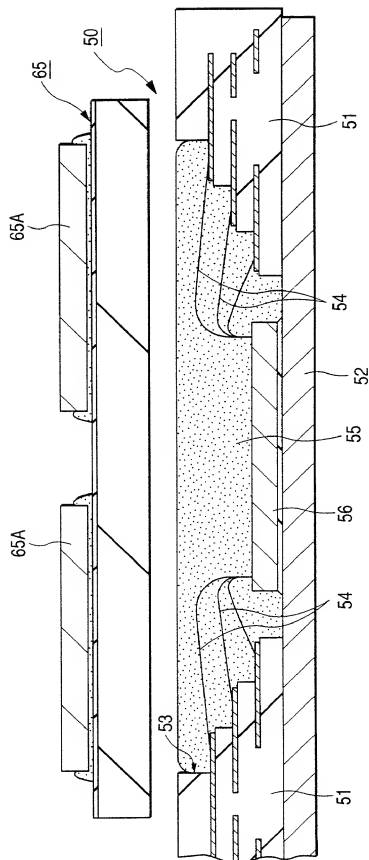


FIG. 16

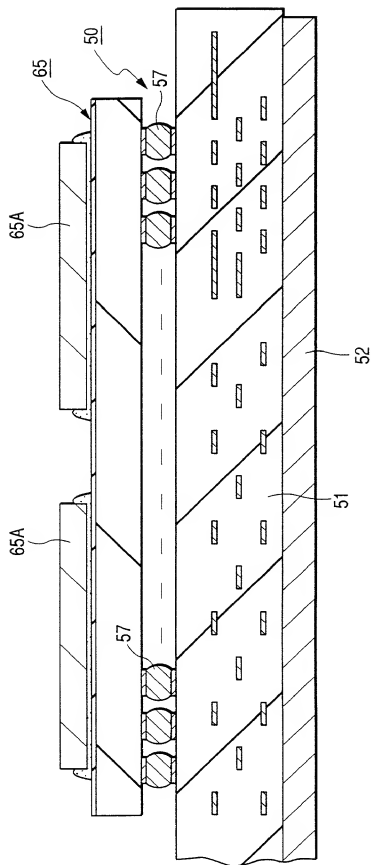
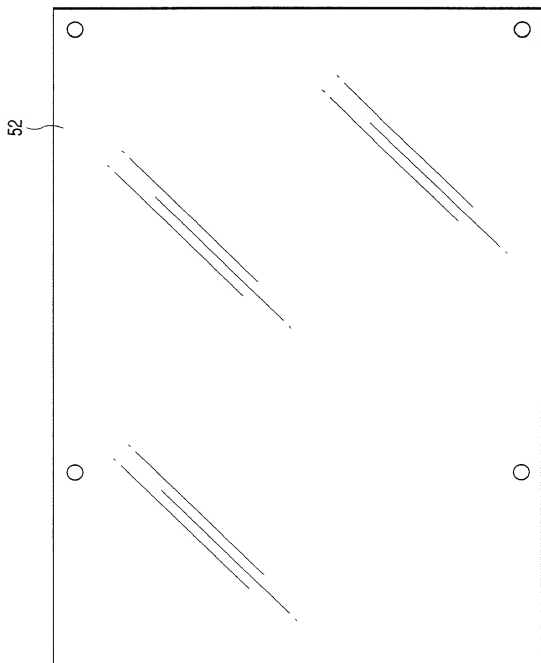


FIG. 17



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Prior Application: S. IMASU et al
Serial No. 09/048,054
Filed: March 26, 1998

Group Art Unit: 2841
Examiner: J. Vigushin
For: PROCESS FOR MOUNTING ELECTRONIC
DEVICE AND SEMICONDUCTOR DEVICE

REQUEST FOR APPROVAL OF PROPOSED DRAWING CORRECTIONS

Assistant Commissioner of Patents
Washington, D.C. 20231

Sir:

The applicants request approval of the proposed drawing corrections as noted on the attached copies of Figs. 1 and 14.

The changes to these figures were requested in the Notice of Draftperson's Patent Drawing Review forwarded with the Office Action dated December 29, 1999 in the parent application, 09/048,054.

Accordingly, the requested changes should be approved.

Applicants submit herewith corrected formal drawings (Figs. 1 and 14) incorporating these changes.

Respectfully submitted,



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Date: January 26, 2001

09/048,054-2841

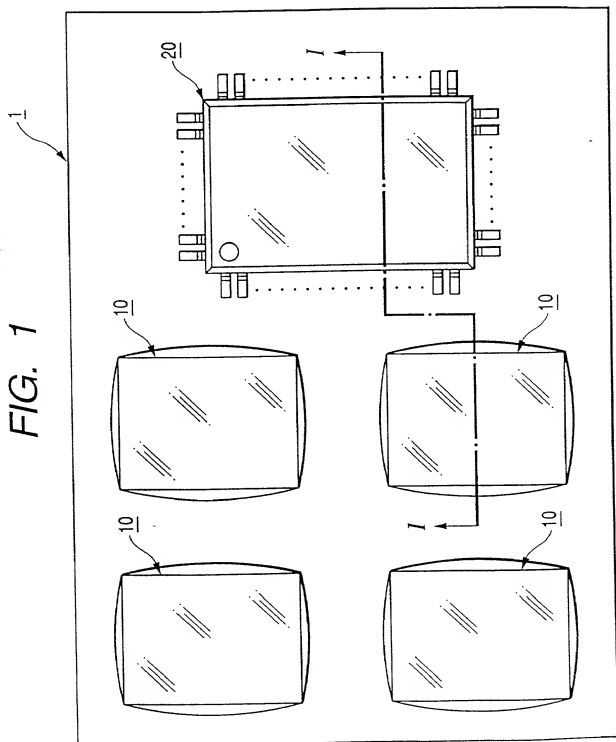


FIG. 14

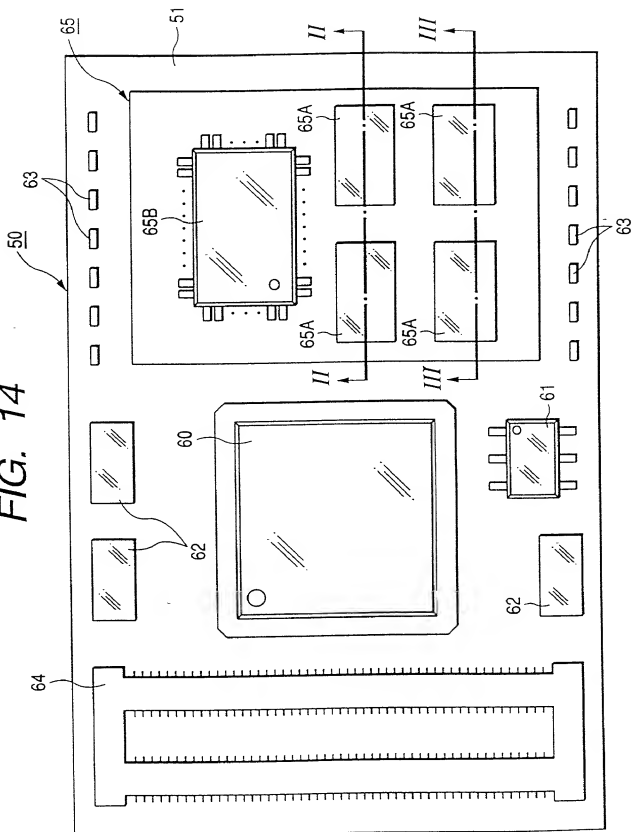


FIG. 1

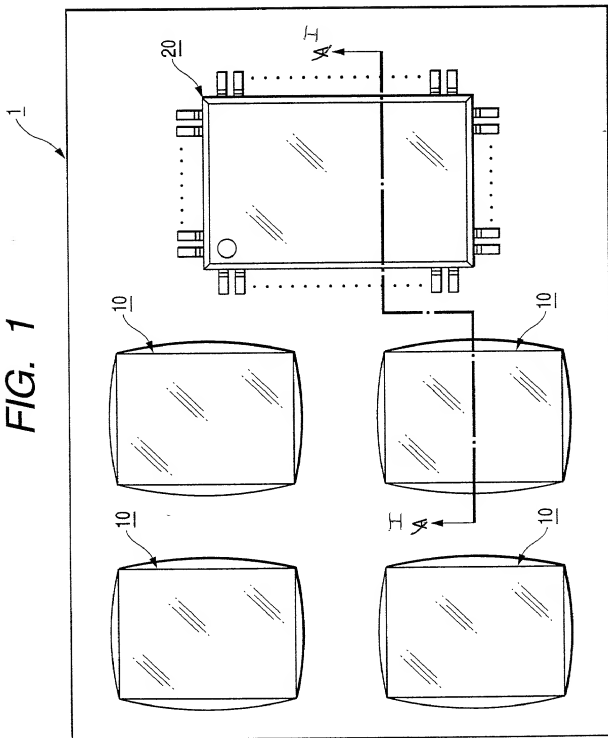


FIG. 14

